

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: St. Lawrence, et al. )  
 ) Group Art Unit: 2826  
Serial No.: 10/613,924 )  
 )  
Filed: July 3, 2003 ) Examiner: Erdem  
 )  
For: CIRCUIT MATERIALS, CIRCUITS, )  
MULTI-LAYER CIRCUITS, AND )  
METHODS OF MANUFACTURE )  
THEREOF )

**VIA First Class Mail**

Assistant Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE**

Sir:

This Response is submitted in response to the Office Action dated February 9, 2006.